[0011] The above summary contains simplifications, generalizations and omissions of detail and is not intended as a comprehensive description of the claimed subject matter but, rather, is intended to provide a brief overview of some of the functionality associated therewith. Other systems, methods, functionality, features and advantages of the claimed subject matter will be or will become apparent to one with skill in the art upon examination of the following figures and detailed written description.

BRIEF DESCRIPTION OF THE DRAWINGS

[0012] The description of the illustrative embodiments can be read in conjunction with the accompanying figures. It will be appreciated that for simplicity and clarity of illustration, elements illustrated in the figures have not necessarily been drawn to scale. For example, the dimensions of some of the elements are exaggerated relative to other elements. Embodiments incorporating teachings of the present disclosure are shown and described with respect to the figures presented herein, in which:

[0013] FIG. **1** is a block diagram illustration of an example information handling system (IHS), according to one or more embodiments;

[0014] FIG. **2** is a block diagram illustrating details of a power management subsystem in an IHS, in accordance with one embodiment;

[0015] FIG. **3** is a block diagram illustrating details of a power supply unit, in accordance with one embodiment;

[0016] FIG. **4** is a block diagram illustrating details of another power management subsystem in an IHS, in accordance with one embodiment;

[0017] FIG. **5** is a block diagram illustrating details of another power supply unit, in accordance with one embodiment;

[0018] FIG. **6**A is a block diagram illustrating example contents of the management controller memory, in accordance with one embodiment;

[0019] FIG. **6**B is a block diagram illustrating example contents of the micro-controller memory, in accordance with one embodiment;

[0020] FIG. **7** is a flow chart illustrating one example of the method by which a management controller selects master and slave power supplies in an IHS, according to one or more embodiments;

[0021] FIG. **8** is a flow chart illustrating one example of the method by which a power supply unit sets the master or slave mode in an IHS, according to one or more embodiments; and

[0022] FIG. **9** is a flow chart illustrating one example of the method by which a power supply micro-controller selects master and slave power supplies in an IHS, according to one or more embodiments.

DETAILED DESCRIPTION

[0023] The illustrative embodiments provide a power supply system, an information handling system (IHS), and a method performed within the IHS to control redundant power supplies.

[0024] In the following detailed description of exemplary embodiments of the disclosure, specific exemplary embodiments in which the disclosure may be practiced are described in sufficient detail to enable those skilled in the art to practice the disclosed embodiments. For example, specific details such as specific method orders, structures, elements, and connections have been presented herein. However, it is to be understood that the specific details presented need not be utilized to practice embodiments of the present disclosure. It is also to be understood that other embodiments may be utilized and that logical, architectural, programmatic, mechanical, electrical and other changes may be made without departing from general scope of the disclosure. The following detailed description is, therefore, not to be taken in a limiting sense, and the scope of the present disclosure is defined by the appended claims and equivalents thereof.

[0025] References within the specification to "one embodiment," "an embodiment," "embodiments", or "one or more embodiments" are intended to indicate that a particular feature, structure, or characteristic described in connection with the embodiment is included in at least one embodiment of the present disclosure. The appearance of such phrases in various places within the specification are not necessarily all referring to the same embodiment, nor are separate or alternative embodiments mutually exclusive of other embodiments. Further, various features are described which may be exhibited by some embodiments and not by others. Similarly, various requirements are described which may be requirements for some embodiments but not other embodiments.

[0026] It is understood that the use of specific component, device and/or parameter names and/or corresponding acronyms thereof, such as those of the executing utility, logic, and/or firmware described herein, are for example only and not meant to imply any limitations on the described embodiments. The embodiments may thus be described with different nomenclature and/or terminology utilized to describe the components, devices, parameters, methods and/or functions herein, without limitation. References to any specific protocol or proprietary name in describing one or more elements, features or concepts of the embodiments are provided solely as examples of one implementation, and such references do not limit the extension of the claimed embodiments to embodiments in which different element, feature, protocol, or concept names are utilized. Thus, each term utilized herein is to be given its broadest interpretation given the context in which that term is utilized.

[0027] Further, those of ordinary skill in the art will appreciate that the hardware components and basic configuration depicted in the various figures (e.g. FIG. **1**) and described herein may vary. For example, the illustrative components within IHS **100** (FIG. **1**) are not intended to be exhaustive, but rather are representative to highlight components that can be utilized to implement various aspects of the present disclosure. For example, other devices/components/modules may be used in addition to or in place of the hardware and software modules depicted. The depicted examples do not convey or imply any architectural or other limitations with respect to the presently described embodiments and/or the general disclosure.

[0028] FIG. **1** illustrates a block diagram representation of an example information handling system (IHS) **100**, within which one or more of the described features of the various embodiments of the disclosure can be implemented. For purposes of this disclosure, an information handling system, such as IHS **100**, may include any instrumentality or aggregate of instrumentalities operable to compute, classify, process, transmit, receive, retrieve, originate, switch, store, display, manifest, detect, record, reproduce, handle, or uti-